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JAN 06 2006

**.IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In Re Application of: )

Mou-Shiung Lin et al. )

Examiner: Jesse A. Fenty

Serial No.: 10/755,042 )

Art Unit: 2815

Filed: 01/09/2004 )

Docket No.: MEG-P0004-USA-1

For: INTEGRATED CHIP PACKAGE )  
STRUCTURE USING SILICON )  
SUBSTRATE AND METHOD OF )  
MANUFACTURING THE SAME )

**AMENDMENT AND RESPONSE TO OFFICE ACTION**

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

The Office Action mailed October 6, 2005 has been carefully considered. In response thereto, please enter the following amendments and consider the following remarks.